

#2/103
D.K. 1/7/02
2812

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of
Yves MORAND et al
Serial No. 09/932,513
Filed: August 17, 2001



Atty Docket 00-CCL-096
Group Art Unit 2812
Confirmation No. 6322

For: PROCESS FOR FABRICATING A CAPACITOR WITHIN AN
INTEGRATED CIRCUIT, AND CORRESPONDING INTEGRATED CIRCUIT

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington, D. C. 20231

SIR,

The attached Form PTO-1449 provides a listing of information which may be relevant to the subject application. This IDS is not intended as a representation that better art is not available nor that the information provided is prior art

This IDS is submitted under
☒ 37 CFR 1.97(b) - No Fee.
☐ 37 CFR 1.97(c) - No Fee, with Certification.
☐ 37 CFR 1.97(c) - Fee
☐ 37 CFR 1.97(d) - Fee, Certification & Petition.

The Commissioner is authorized to charge any required fees under 37 CFR 1.17(p) and (i) (1) to Deposit Account No. 50-1556.

Respectfully submitted

Date

10/15/01

By

Jose Gutman

Registration No. 35 171

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CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service by first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, DC 20231.

Kathleen Smith

Name of Person Mailing Paper

Kathleen Smith

Signature of Person Mailing Paper

10/15/01

Date of Deposit